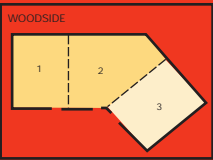
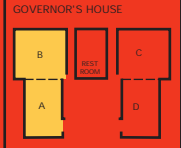



	Sunday	Monday	Tuesday	Wednesday
8:00	SCHEDULE @ A GLANCE	Registration	Registration	Registration
8:30			A. Karp, HP	F. Baetke, Technical Computing Division, HP
8:45		Welcome, D. Frederick, San Diego Supercomputer Center	E-Services - Does it Mean Anything for High-Performance Computing	A Critical Review: SMPs, Clusters and ccNUMA Architectures
9:00		D. Zitzner, President, Computing Systems, HP HP and Technical Computing	B. Cox, Product Manager, Technical/Scientific Servers, HP HPC-System Strategy - UNIX Server Platforms	P. Woodward, U of Minnesota From Vectors to RISC - New Practices and Experiences
9:30		J. Chaffin, Vice President & General Manager, Business Critical Computing, HP The Next Step: Integrating High-Performance Computing and E-Services	S. McKaig, HP HP-UX 11 - Status and New Features for HPC	K. Wadleigh and I. Crawford, HP HP's Mathematical Library MLIB and Future Directions
10:00		Break	Break	Break
10:30		B. Toal, Marketing Director, Technical Computing Division, HP Technical Computing - Strategy and Successes	G. Grundler, HP Linux on HP's PA-RISC Systems - Status and Roadmap	R. Collier and M. Bunde, Etnus How to Easily Debug MPI Programs on your HP
11:00		Registration	C. Willard, International Data Corporation Capability vs. Capacity - Singles System Image vs. SMP-Clusters	S. Eranian, HP The Linux/IA64 Project: Past, Present, Future
11:30			S. Williams, HP Labs Looking Beyond the Horizon - An Approach to Nano-Technology	I. Lumb, Platform Computing 'Mission Critical' Technical Computing
11:45				M. Bartelt, Caltech Experiences with LSF
12:00		Lunch	D. an May and S. Schmidt, Aachen U of Tech HP's Software Development Environment: Practice and Experiment	
12:00		Lunch	Lunch	
13:00	Tutorial A Kuck & Associates OpenMP Techniques and Tools for HP Shared Memory Systems	J. Sander, Department of Energy ASCI PathForward: Industrial Alliances to Accelerate High End Computing	K. Jenkins, HP HP's Storage & File Services Strategy	G. Astfalk, Chief Scientist, Technical Computing Division, HP Saving ALL of Society's Data
13:30		J. Reilly, Intel Measuring Performance of Processors and Servers - The SPEC2000 Suite		
14:00		Break	F. Kim, Unifree High Performance Computing and Mass Storage	Q&A Session
14:30		Contributed Papers (Parallel Sessions) Session A	M. Krause, HP PCIX & Infiniband - A HP Perspective	
15:00		S. Brunett, Caltech	Break	HPCUG Business Meeting
15:15	Break	T. Racic, ETH		
15:30	Tutorial B Platform Computing The LSF Package - Features, Functions and Future Development of the HP-UX 11 Implementation	P. Junglas, TUHH L. Rauchwerger, Texas A&M Session B S. Dong, U of Kentucky B. Cheng, JPL S. Deshpande, Lucent	S. Rowan, HP Development Software (C,C++,F90, JAVA & Tools)	Adjourn / Announcement of HPCUG 2001
16:00		Depart for Cruise	Kuck & Associates OpenMP - Further Developments of a New Standard	SCHEDULE @ A GLANCE    hewlett-packard's 2001 HPCUG San Jose, California, March 19-22 http://www.cacr.caltech.edu/HPCUG2001
16:30			H. Strauss, HP Comparing Parallel Programming Paradigms and Their Application	
17:00			P. McDaniel, HP Achieving High Availability	
17:30			Depart for HP System Design Lab Tour and Beer Party	
17:45	Special Lecture A. Potler & K. Wadleigh, HP Comparing IA-64 and PA-RISC: An Introduction to a New Instruction Set	Board San Francisco Spirit for Bay Cruise	HP System Design Lab Tour	
18:00				
18:30				
18:45				
19:00	Reception			
19:30		T. Sterling, California Institute of Technology & Jet Propulsion Laboratory Beowulf - A Challenge for Classical High-End Systems?	Depart for Beer Party	
20:00		Buses depart for Hyatt	Beer Party	